#### APPLICATION FOR UNITED STATES LETTERS PATENT

#### **FOR**

# RETURNING RINGING SIGNALS THROUGH THE TIP AMPLIFIER OF SUBSCRIBER-LINE INTERFACE CIRCUITRY

Inventors:

John C. Gammel

David Chabinec Dean Umberger

Prepared by:

Mendelsohn & Associates, P.C. 1515 Market Street, Suite 715

Philadelphia, Pennsylvania 19102

(215) 557-6657 Customer No. 22186

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# RETURNING RINGING SIGNALS THROUGH THE TIP AMPLIFIER OF SUBSCRIBER-LINE INTERFACE CIRCUITRY

# Cross-Reference to Related Applications

This application claims the benefit of the filing date of U.S. provisional application no. 60/406,550, filed on 08/27/02.

#### **BACKGROUND OF THE INVENTION**

# Field of the Invention

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The present invention relates to telecommunications, and, in particular, to subscriber-line interface circuits (SLICs) for telephones and other customer premises equipment (CPE).

# Description of the Related Art

When located at a central office (CO), subscriber-line interface circuits provide the electrical interface between the CO and its customers' telephones and/or other CPE. Fig. 1 shows a high-level schematic diagram of a portion of a prior-art SLIC 100 used to support telecommunications services for an individual customer.

In particular, Fig. 1 shows tip amplifier 102 and ring amplifier 104, which are connected between the rest of the SLIC's audio-feeding circuitry 106 and tip and ring lines 108 and 110, respectively, which lines provide the electrical connection to the customer's CPE. Connected between tip line 108 and tip amp 102 is switch S3, while switch S4 switchably connects tip line 108 to ground. Similarly, connected between ring line 110 and ring amp 104 is switch S2, while switch S1 switchably connects ring line 110 to ground through ringing power supply 112, which, according to a conventional analog POTS (plain old telephone service) signaling protocol, provides ringing and a 48V DC voltage offset between ground and one side of switch S1. Depending on the implementation, the voltage applied to switch S1 by power supply 112 may be either positive or negative.

When the customer's CPE is on hook, the CPE is configured such that tip and ring lines 108 and 110 form an open circuit path through the CPE. When the customer's CPE is off hook, the configuration of CPE changes such that tip and ring lines 108 and 110 form a closed circuit path though the CPE. When a call is placed to the customer's CPE (e.g., when the CPE is on hook), SLIC 100 provides a ringing signal to the CPE via ring line 110, which signal returns to SLIC 100 (e.g., when the CPE goes off hook) via tip line 108.

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During ringing, switches S1 and S4 are closed, and switches S2 and S3 are open. As such, the ringing voltage from power supply 112 is applied to the customer's CPE via ring line 110. The ringing signal is an intermittent AC signal (e.g., 88V rms) superimposed by power supply 112 on the 48V DC voltage offset.

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When the CPE goes off hook (e.g., when the customer answer his/her phone), tip and ring lines 108 and 110 are connected via the CPE, which starts to draw DC current from power supply 112. Depending on the implementation, the ringing signal should be returned to the SLIC via tip line 108 either to ground or to battery. For example, in prior-art SLIC 100, the ringing signal is returned to ground via switch S4.

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When the SLIC detects that the CPE is off hook (e.g., by detecting the DC current drawn from power supply 112), switches S1 and S4 are opened, and switches S2 and S3 are closed to connect tip and ring lines 108 and 110 to receive the audio signals provided by audio-feeding circuitry 106 via tip and ring amps 102 and 104, respectively.

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In the past, switches S1-S4 were implemented using a double-pull, double-throw electromechanical relay having four poles. In more modern, integrated-circuit implementations, each of switches S1-S4 is implemented as a set of one or more relatively large transistors that are capable of handling the voltages involved in analog POTS service.

# SUMMARY OF THE INVENTION

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In accordance with the principles of the present invention, subscriber-line interface circuitry returns the ringing signal (to ground or to battery, depending on the implementation) via the tip amplifier. As such, SLIC circuitry can be implemented using only three of the four switches used in prior-art SLIC circuitry. (For example, in the context of SLIC 100 of Fig. 1, by returning the ringing signal to ground via the tip amp, switch S4 is not needed and can be eliminated from the circuitry.) Since one of the switches is eliminated, SLIC circuitry of the present invention can be smaller and therefore cheaper to implement.

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In one embodiment, the present invention is subscriber-line interface circuitry (SLIC) for connecting customer premises equipment (CPE) to a telecommunications network via a tip-and-ring line pair. The SLIC comprises audio-feeding circuitry and ringing circuitry. The audio-feeding circuitry is adapted to transmit and receive audio signals to and from the CPE via the tip and ring lines. The ringing circuitry is adapted to provide a ringing signal to the CPE via the ring line. An output side of the audio-feeding circuitry includes a tip amplifier adapted to be connected to the tip line. The SLIC is adapted to return the ringing signal from the CPE via the tip line and through the tip amplifier.

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In another embodiment, the present invention is, in a telecommunications network, a method for interfacing with customer premises equipment (CPE) via a tip-and-ring line pair. A ringing signal is provided to the CPE via the ring line. The ringing signal is received from the CPE via the tip line, wherein the ringing signal is returned through a tip amplifier of a SLIC of the telecommunications network.

# BRIEF DESCRIPTION OF THE DRAWINGS

Other aspects, features, and advantages of the present invention will become more fully apparent from the following detailed description, the appended claims, and the accompanying drawings in which like reference numerals identify similar or identical elements.

Fig. 1 shows a high-level schematic diagram of a portion of a prior-art subscriber-line interface circuit (SLIC) used to support telecommunications services for a particular customer;

Fig. 2 shows a high-level schematic diagram of a portion of a SLIC, according to one embodiment of the present invention; and

Fig. 3 shows a schematic diagram of the front end of the tip amp of Fig. 2.

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# **DETAILED DESCRIPTION**

Reference herein to "one embodiment" or "an embodiment" means that a particular feature, structure, or characteristic described in connection with the embodiment can be included in at least one embodiment of the invention. The appearances of the phrase "in one embodiment" in various places in the specification are not necessarily all referring to the same embodiment, nor are separate or alternative embodiments necessarily mutually exclusive of other embodiments.

Fig. 2 shows a high-level schematic diagram of a portion of a subscriber-line interface circuit 200, according to one embodiment of the present invention. Like SLIC 100 of Fig. 1, SLIC 200 has a tip amp 202 and a ring amp 204 connected between the rest of the SLIC's audio-feeding circuitry 206 and tip and ring lines 208 and 210, respectively, via switches S3 and S2. Moreover, like SLIC 100, SLIC 200 has a power supply 212 connected between ground and ring line 210 via a switch S1. Those skilled in the art will appreciate that SLIC 200 has additional circuitry that supports the overall functionality of the SLIC. The elements shown in Fig. 2 are intended to illustrate the present invention and are not intended to be a complete depiction of the SLIC's circuitry.

One difference between SLIC 100 and SLIC 200 is the absence of a fourth switch (analogous to switch S4 of Fig. 1) connecting tip line 208 to ground (or to battery). According to embodiments of the present invention, depending on the implementation, the ringing signal is returned from tip line 208 to ground or to battery through tip amp 202.

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In particular, during ringing, switches S1 and S3 are closed, and switch S2 is open, such that, when the customer's CPE goes off hook, the ringing signal provided to the CPE from power supply 212 via ring line 210 is returned from the CPE via tip line 208, through switch S3, and into tip amp 202. When SLIC 200 detects that the CPE went off hook, switch S1 is opened, and switch S2 is closed to connect the CPE to the SLIC's audio-feeding circuitry. In a preferred implementation, in order to reduce power consumption, tip amp 202 is driven to saturation (either to ground or to battery) during ringing.

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Fig. 3 shows a schematic diagram of the front end of tip amp 202 of Fig. 2, according to one embodiment of the present invention. In particular, Fig. 3 shows circuitry (comprising transistors T1 and T2, diodes D1 and D2, and DC power supply 302) connected between switch S3 of Fig. 2 (at node N1) and the rest of tip amp 202 (304).

In one mode of operation, during ringing, transistor T1 is turned on and transistor T2 is turned off. As such, the ringing signal returned from the customer's CPE via tip line 208 of Fig. 2 via switch S3 will be driven to ground through diode D1. When the ringing signal is drawing current from the SLIC, the current flows from ground through transistor T1.

In another mode of operation in which the ringing signal is returned to battery, transistor T1 would be turned off and transistor T2 would be turned on. In that case, when the ringing signal is providing current to the SLIC, the current flows through transistor T2 to ground via DC power supply 302. When the ringing signal is drawing current from the SLIC, the current flows from ground via DC power supply 302 through diode D2.

Although Fig. 3 shows circuitry that supports two different modes of operation in which the ringing signal can be returned to either ground or to battery, in alternative implementations, only one of these return options might be supported.

The present invention has been described in the context of circuitry having three switches S1, S2, and S3, as in Fig. 2. In an alternative embodiment, switch S3 may be eliminated (e.g., when there is no need to ever disconnect tip line 208 from tip amp 202).

The present invention may be implemented as circuit-based processes, including possible implementation as a single integrated circuit, a multi-chip module, a single card, or a multi-card circuit pack. As would be apparent to one skilled in the art, various functions of circuit elements may also be implemented as processing steps in a software program. Such software may be employed in, for example, a digital signal processor, micro-controller, or general-purpose computer.

Unless explicitly stated otherwise, each numerical value and range should be interpreted as being approximate as if the word "about" or "approximately" preceded the value of the value or range.

It will be further understood that various changes in the details, materials, and arrangements of the parts which have been described and illustrated in order to explain the nature of this invention may be

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made by those skilled in the art without departing from the scope of the invention as expressed in the following claims.

Although the steps in the following method claims, if any, are recited in a particular sequence with corresponding labeling, unless the claim recitations otherwise imply a particular sequence for implementing some or all of those steps, those steps are not necessarily intended to be limited to being implemented in that particular sequence.

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